

# United States Patent [19]

Ogawa et al.

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#### [54] SYSTEM FOR FAST DATA TRANSFER UTILIZING SEPARATION OF TRANSFER DATA HEADER AND SUBSTANTIALLY CONTINUOUSLY READING AND PROCESSING TRANSFER DATA BASED ON READ HEADER

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[52]	U.S. Cl	<b>395/878</b> ; 395/280; 395/293;		
		395/307; 370/60		
[58]	Field of Search	395/307, 293,		
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#### [56] References Cited

#### U.S. PATENT DOCUMENTS

5,128,931 7/1992 5,263,024 11/1993 5,291,486 3/1994 5,497,404 3/1996 5,524,054 6/1996 5,526,495 6/1996	Yoshioka 340/798   Yamanaka et al. 370/60   Kumaki et al. 370/94.1   Koyanagi 370/84   Grover et al. 375/357   Spille 381/18   Shibata et al. 395/307   Gentry et al. 395/200.64
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#### FOREIGN PATENT DOCUMENTS

41 22 831 A1 1/1992 Germany . WO 94/07196 3/1994 WIPO.

### OTHER PUBLICATIONS

"Fire, Fibre, SSA", Schnurer, in c't, No. 6, 1995, pp. 126-134.

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#### **ABSTRACT**

A data processing apparatus suitable for fast processing of a large amount of data provides continuous and real-time reproduction of moving pictures that have a reality to them. In one embodiment, the data processing apparatus includes: a first memory for receiving and storing first transfer data supplied from an external device in a first transfer mode for transferring data in a predetermined cycle; a second memory for receiving and storing second transfer data supplied from the external device in a second transfer mode, the second transfer mode transfers the second transfer data using a remaining time resulting from exclusion of a time needed for transfer of the first transfer data from the predetermined cycle; a first interface for receiving the first transfer data read from the first memory and outputting the first transfer data; and a second interface for receiving the second transfer data read from the second memory and outputting the second transfer data.

## 12 Claims, 11 Drawing Sheets

